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(72) Inventors:
• **Norskov, Soren**
2200 Kobenhavn N (DK)
• **Rasmussen, Carsten**
2450 Copenhagen SV (DK)

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(71) Applicant: **Nokia Corporation**
02150 Espoo (FI)

(74) Representative: **Jones, Kendra Louise et al**
Nokia IPR Department,
Nokia House,
Summit Avenue
Southwood, Farnborough,
Hampshire GU14 0NZ (GB)

(54) **Esd protection of integrated circuit**

(57) Chip assembly comprising a chip mounted on a support, said chip assembly comprising at least one terminal and discharge means for discharging a charge

accumulated on or transferred to said terminal. Said discharge means is connected to an RF common mode node for at least one circuit on the chip.

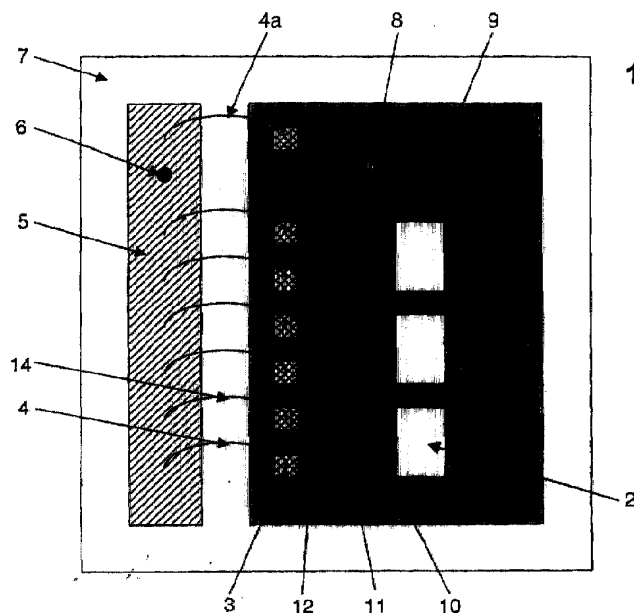


Fig. 1



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EUROPEAN SEARCH REPORT

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EP 00 31 0660

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The present search report has been drawn up for all claims			
Place of search BERLIN		Date of completion of the search 19 November 2002	Examiner Polesello, P
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermed ale document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	

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Application Number
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<p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p>			
<p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document</p>			

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